



**P-channel 20V, DFN2\*2-6 MOSFET P-溝道場效應管**

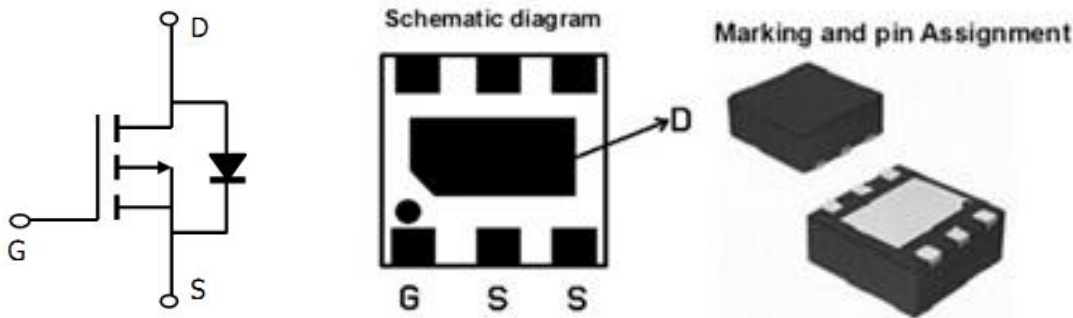
**■Features 特點**

Low gate charge 低柵極電荷密度  
Advanced trench technology 優秀溝槽技術  
Backside heat sink 背面熱沉  
 $R_{DS(ON)} \leq 45m\Omega @ V_{GS} = -4.5V$   
 $R_{DS(ON)} \leq 60m\Omega @ V_{GS} = -2.5V$

**■Applications 應用**

Load Switch 負載開關  
PWM 脈寬調制應用  
Power Management 電源管理

**■Internal Schematic Diagram 內部結構**



**■Absolute Maximum Ratings 最大額定值**

Characteristic 特性參數	Symbol 符號	Rating 額定值	Unit 單位
Drain-Source Voltage 漏極-源極電壓	$BV_{DSS}$	-20	V
Gate- Source Voltage 柵極-源極電壓	$V_{GS}$	$\pm 10$	V
Drain Current (continuous)漏極電流-連續	$I_D$	-7	A
Drain Current (pulsed)漏極電流-脈沖	$I_{DM}$	-15	A
Total Device Dissipation 總耗散功率	$P_{TOT}(at T_C = 25^\circ C)$	2.7	W
Thermal Resistance Junction-Ambient 熱阻	$R_{\theta JA}$	25	$^\circ C/W$
Junction/Storage Temperature 結溫/儲存溫度	$T_J, T_{stg}$	-55~150	$^\circ C$



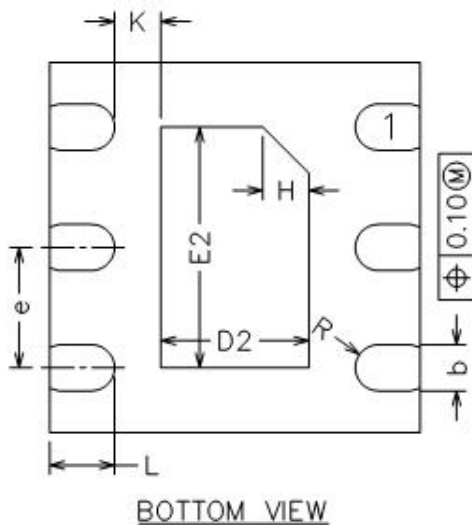
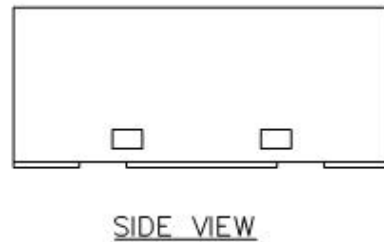
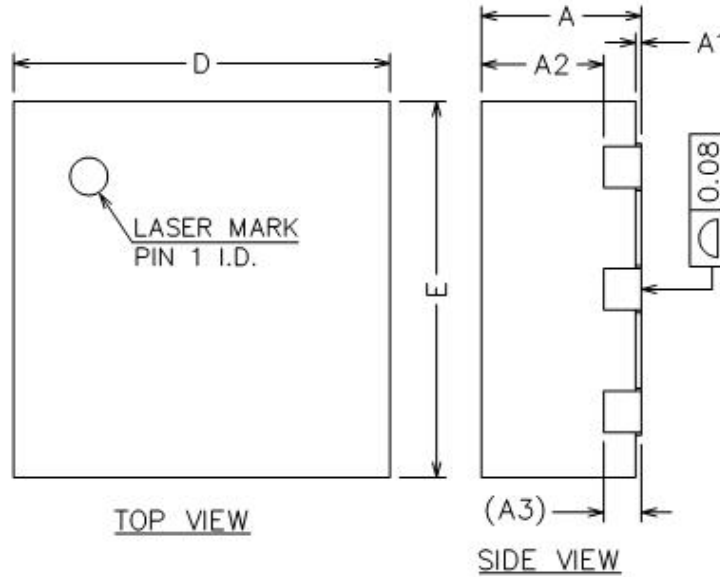
■ Electrical Characteristics 電特性

( $T_A=25^{\circ}\text{C}$  unless otherwise noted 如無特殊說明，溫度為  $25^{\circ}\text{C}$ )

Characteristic 特性參數	Symbol 符號	Min 最小值	Typ 典型值	Max 最大值	Unit 單位
Drain-Source Breakdown Voltage 漏極-源極擊穿電壓( $I_D=-250\mu\text{A}, V_{GS}=0\text{V}$ )	$BV_{DSS}$	-20	—	—	V
Gate Threshold Voltage 柵極開啓電壓( $I_D=-250\mu\text{A}, V_{GS}=V_{DS}$ )	$V_{GS(th)}$	-0.5	—	-1.5	V
Zero Gate Voltage Drain Current 零柵壓漏極電流( $V_{GS}=0\text{V}, V_{DS}=-16\text{V}$ )	$I_{DSS}$	—	—	-1	$\mu\text{A}$
Gate Body Leakage 柵極漏電流( $V_{GS}=\pm 8\text{V}, V_{DS}=0\text{V}$ )	$I_{GSS}$	—	—	$\pm 100$	nA
Static Drain-Source On-State Resistance 靜態漏源導通電阻( $I_D=-5\text{A}, V_{GS}=-4.5\text{V}$ ) ( $I_D=-3\text{A}, V_{GS}=-2.5\text{V}$ )	$R_{DS(on)}$	—	—	45 60	$\text{m}\Omega$
Diode Forward Voltage Drop 內附二極管正向壓降( $I_{SD}=-1\text{A}, V_{GS}=0\text{V}$ )	$V_{SD}$	—	—	-1	V
Forward Transfer Admittance 正向傳輸導納 ( $V_{DS}=-5\text{V}, I_D=-3.5\text{A}$ )	$g_{FS}$	—	8	—	S
Input Capacitance 輸入電容 ( $V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$ )	$C_{ISS}$	—	600	—	pF
Common Source Output Capacitance 共源輸出電容( $V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$ )	$C_{OSS}$	—	120	—	pF
Reverse Transfer Capacitance 反向傳輸電容 ( $V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$ )	$C_{RSS}$	—	80	—	pF
Gate Source Charge 柵源電荷密度 ( $V_{DS}=-10\text{V}, I_D=-3\text{A}, V_{GS}=-4.5\text{V}$ )	$Q_{gs}$	—	5.5	—	nC
Gate Drain Charge 柵漏電荷密度 ( $V_{DS}=-10\text{V}, I_D=-3\text{A}, V_{GS}=-4.5\text{V}$ )	$Q_{gd}$	—	3.3	—	nC
Turn-On Delay Time 開啓延遲時間 ( $V_{DS}=-10\text{V}, R_L=1.8\Omega, R_{GEN}=3\Omega, V_{GS}=-4.5\text{V}$ )	$t_{d(on)}$	—	5	—	ns
Turn-On Rise Time 開啓上升時間 ( $V_{DS}=-10\text{V}, R_L=1.8\Omega, R_{GEN}=3\Omega, V_{GS}=-4.5\text{V}$ )	$t_r$	—	2	—	ns
Turn-Off Delay Time 關斷延遲時間 ( $V_{DS}=-10\text{V}, R_L=1.8\Omega, R_{GEN}=3\Omega, V_{GS}=-4.5\text{V}$ )	$t_{d(off)}$	—	16	—	ns
Turn-On Fall Time 開啓下降時間 ( $V_{DS}=-10\text{V}, R_L=1.8\Omega, R_{GEN}=3\Omega, V_{GS}=-4.5\text{V}$ )	$t_f$	—	2	—	ns



■ DIMENSION 外形封裝尺寸



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.80	0.85	0.90
A1	0.00	0.02	0.05
A2	0.60	0.65	0.70
A3	0.20REF		
b	0.18	0.25	0.30
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D2	0.70	0.80	0.90
E2	1.20	1.30	1.40
e	0.55	0.65	0.75
H	0.25REF		
K	0.20	-	-
L	0.30	0.35	0.40
R	0.11	-	-